

Figure 1
(Prior Art)

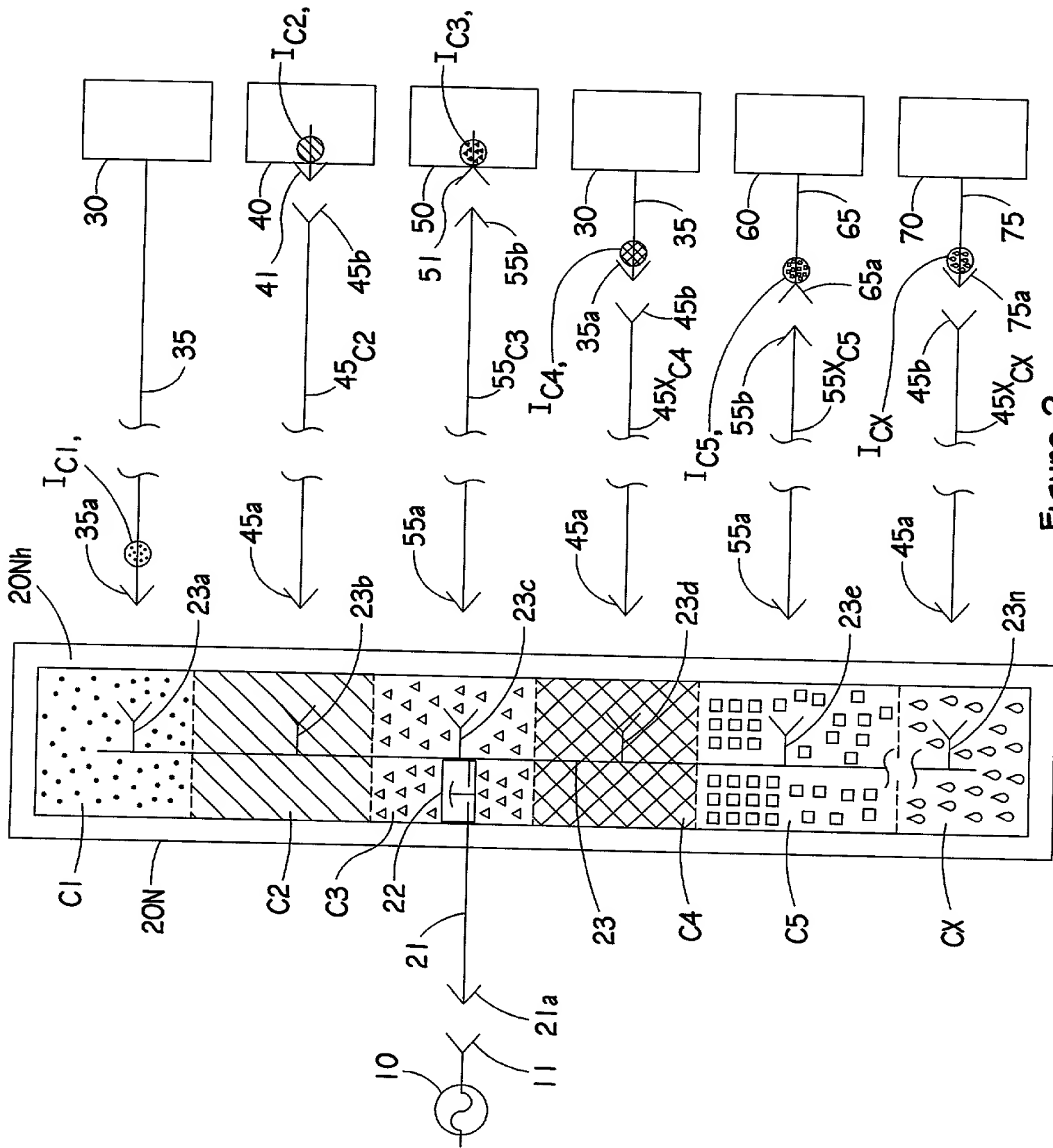


Figure 2

FIG. 2 is a schematic diagram of a multi-layered device structure. The main body is a rectangular block divided into vertical sections labeled C1, 20N, C2, C3, 22, 23, C4, C5, and CX. Section 20N is further divided into sub-sections 23a, 23b, 23c, 23d, 23e, and 23n. A central vertical channel 21 is shown, with a cross-section 21a. At the top, a circular component 10 is connected to a line 11, which then branches into lines 21 and 21a. Various electrical components and connections are labeled: IC1, 35a, 30, 35, 45a, 41, 40, 45C2, 45b, IC2, 51, 50, 55C3, 55b, IC3, IC4, 35a, 45XC4, 45b, 35, IC5, 55b, 55XC5, 65, ICX, 65a, 70, 75, 45b, 45XCX, 75a. The diagram uses various patterns (dots, diagonal lines, triangles, squares) to represent different materials or layers within the sections.

FIG. 3 is a perspective view of the device 20N, showing the device 20N in a perspective view.

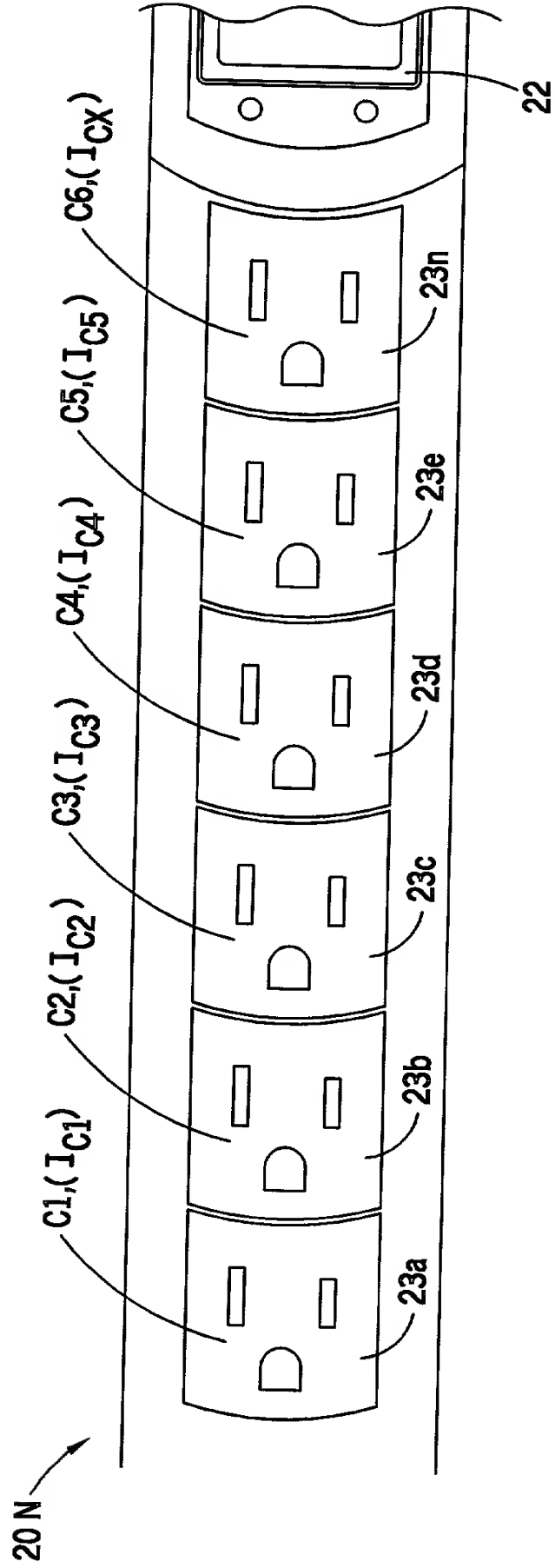


Figure 3